OSP-9961

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Hiroshi Ryu and Yusuke Katch

Serial No. 09/   Group Art Unit (unassigned)

Filed December 1, 2000   Examiner (unassigned)

For METHOD, AND APPARATUS FOR
     SIMULATING A SYSTEM USING AN
     OBJECT ORIENTED LANGUAGE

Box New Application
Assistant Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

     Prior to the Office Action, please amend the above-identified patent

application as follows:

In the claims:

1   Claim 9 (Amended). A simulation apparatus, using a computer, for

2   [executing the method according to any one of claims 1 to 8] simulating a

3   system having a plurality of circuit modules, said simulation apparatus

4   comprising:

5   storage means storing a plurality of circuit base classes, which

6   describe base circuit modules as classes, as a library;

7   means accessing the storage means and describing the circuit

8   modules to be simulated as classes by inheriting the circuit base classes

9   stored in the library; and

10   means combining the circuit modules descried as classes to

11   describe the system to be simulated.